

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE JAN 20 2006**

In re application of: Rinerson et al.

Attorney Docket No.: P020.03.AB

Application No.: 10/634,636

Examiner: Son Luu Mai

Filed: August 4, 2003

Group: 2818

Title: A 2-Terminal Trapped Charge Memory  
Device with Voltage Switchable Multi-Level  
Resistance

Confirmation No. 7199

**CONSENT OF ASSIGNEE TO CORRECTION OF INVENTORSHIP  
and RIGHT OF ASSIGNEE TO TAKE ACTION  
37 C.F.R. 1.48(a) and 3.73(b)**

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Unity Semiconductor Corporation, the assignee of the entire right, title, and interest of the above-captioned application as evidenced by an assignment recorded at Reel 014138 Frame 0208 in the United States Patent and Trademark Office, hereby consents to the correction of inventorship in the above-captioned application in accordance with the papers filed herewith under 37 C.F.R. §1.48. The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

Date: 8-15-05

Darrell R.  
Darrell Rinerson  
President, CEO and Chairman

01/23/2006 MBINAS 00000013 10634636

01 FC:1808

130.00 OP

JAN 20 2006

**SUPPLEMENTAL DECLARATION FOR U.S. PATENT APPLICATION**

Attorney's Docket No. P020.03.AB

I hereby declare that:

Each inventor's residence, mailing address, and citizenship are as stated below next to their name.

I believe the inventor(s) named below to be the original and first inventor(s) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

A 2-Terminal Trapped Charge Memory Device with Voltage Switchable Multi-Level Resistance,

the specification of which was filed on 8/4/2003 as United States Application Number or PCT International Application Number 10/634,636.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment specifically referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application.

**Prior Foreign Application(s)**

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or (f), or 365(b) of any foreign application(s) for patent, inventor's or plant breeder's rights certificate(s), or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent, inventor's or plant breeder's rights certificate(s), or any PCT international application having a filing date before that of the application on which priority is claimed.

(Appl. No.)	(Country)	(Date Filed)	Priority Claimed?	Certified Copy Attached?
			<input type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Yes <input type="checkbox"/> No

**Direct all Correspondence To:****Customer Number: 42958**

42958

**Direct Telephone Calls To:**

Morgan Malino at telephone number (408) 737-7200

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full Name of Sole or First Inventor:	Darrell RINERSON	Citizenship:	USA
Inventor's signature:	<u>Darrell R.</u>	Date of Signature:	8-15-05
Residence: (City)	Cupertino	(State/Country)	CA
Post Office Address:	10423 Henry Creek Place, Cupertino, CA 95014		

Full Name of Second Joint Inventor (if any):

Christophe J. CHEVALLIER

Citizenship: USA

Inventor's signature:

Christophe J. Chevallier

Date of Signature: 8/17/05

Residence: (City)

Palo Alto

(State/Country) CA

Post Office Address:

168 Tennyson Avenue, Palo Alto, CA 94301

Full Name of Third Joint Inventor (if any):

Wayne KINNEY

Citizenship: USA

Inventor's signature:

Wayne Kinney

Date of Signature: 8-27-05

Residence: (City)

Emmett

(State/Country) USA

Post Office Address:

7506 Upper Ave, Emmett, ID 83617

Full Name of Forth Joint Inventor (if any):

Steven W. LONGCOR

Citizenship: USA

Inventor's signature:

Steve W. Longcor

Date of Signature: 8/29/05

Residence: (City)

Mountain View

(State/Country) CA

Post Office Address:

2711 Levin Court, Mountain View, CA 94040

Full Name of Fifth Joint Inventor (if any):

Edmond R. WARD

Citizenship: USA

Inventor's signature:

Edmond R. Ward

Date of Signature: 8/19/05

Residence: (City)

Monte Sereno

(State/Country) CA

Post Office Address:

17324 Eaton Lane, Monte Sereno, CA 95030

C-2006  
JUL 26 2006**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Rinerson et al.

Attorney Docket No.: P020.03.AB

Application No.: 10/634,636

Examiner: Son Luu Mai

Filed: August 4, 2003

Group: 2818

Title: A 2-Terminal Trapped Charge Memory  
Device with Voltage Switchable Multi-Level  
Resistance

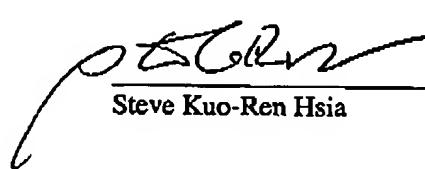
Confirmation No.: 7199

**STATEMENT OF PERSON BEING DELETED  
AS INVENTOR 37 C.F.R. 1.48(a)**

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

I, Steve Kuo-Ren Hsia, hereby state that I was incorrectly named as an inventor in the above-referenced patent application, and that this error in inventorship occurred without deceptive intention on my part.

Date: 8/15/05  
Steve Kuo-Ren Hsia